

9/18/97

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:  
W. SLEMMER  
  
Serial No.  
  
Filed: HEREWITH  
  
For: DIRECT CURRENT SUM  
BANDGAP VOLTAGE  
COMPARATOR

§  
§  
§ Group Art Unit:  
§  
§ Examiner:  
§  
§ Attorney Dkt. No.: 93-C-07C2  
§  
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§

#25/Pre  
I (NE)  
11/4/97  
AS

Certificate of Mailing Under 37 C.F.R. § 1.8(a)  
I certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class mail in an envelope addressed to: Assistant Commissioner of Patents, Washington, D.C. 20231 on September 15, 1997.  
  
By: Carolyn Wright  
Carolyn Wright

PRELIMINARY AMENDMENT

Box: FWC  
Assistant Commissioner of Patents  
Washington, D.C. 20231

Sir:  
  
Prior to examination of the above-identified continuation application, please amend the application as follows. A check for the required fees is enclosed.

IN THE CLAIMS:

Please cancel claims 1, 4-14, 17-26 and 30.  
Please amend the claims as follows:

Do not  
enter,  
(see paper  
#26).  
TDC  
12/1/96

- 1 27. (twice amended) A direct current sum bandgap voltage  
2 comparator comprising:  
3 a power supply having a predetermined threshold voltage  
4 level which defines the minimum acceptable voltage level of the  
5 power supply;  
6 a summing node;  
7 a plurality of current sources connected to the summing node  
8 and directly connected to a power supply voltage, each current  
9 source further comprising at least one field effect transistor  
10 and at least one bipolar junction transistor and each current